FIG. 1

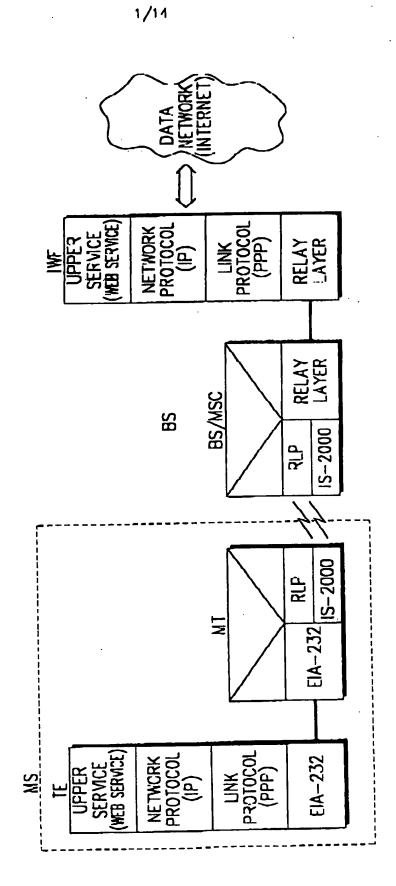
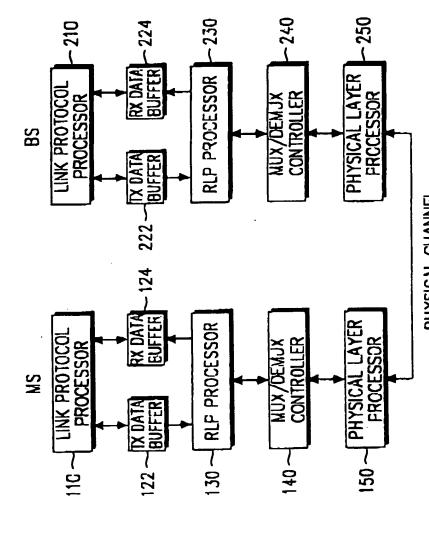


FIG. 2



PHYSIÇAL CHANNEL

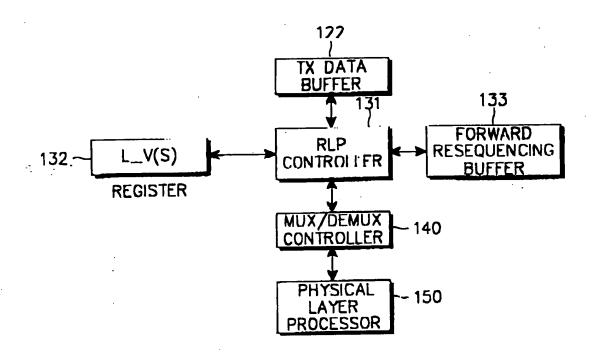


FIG. 3

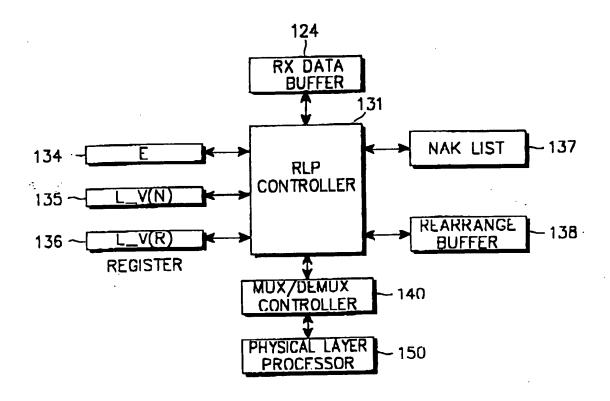


FIG. 4

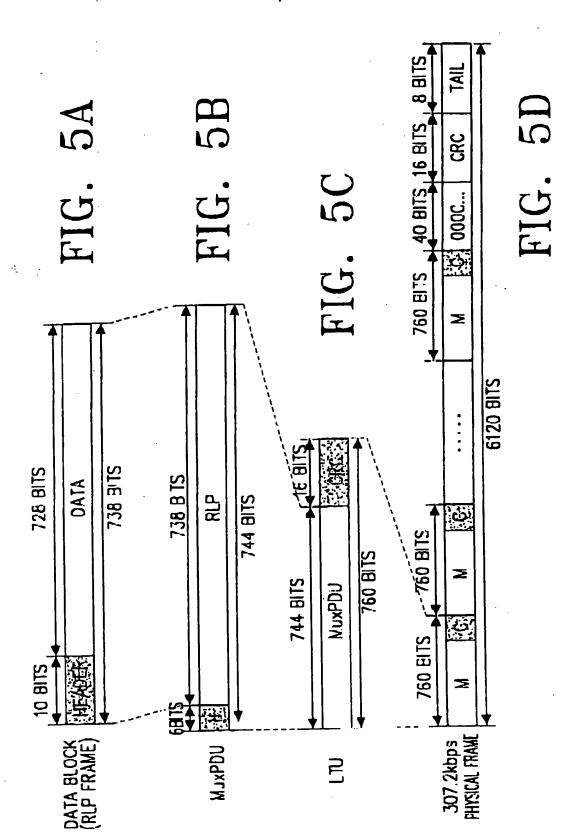


FIG. 6A

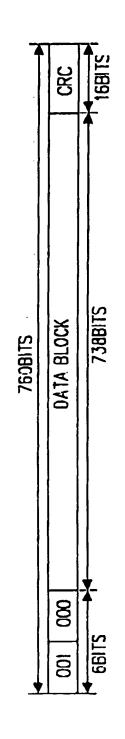


FIG. 6B

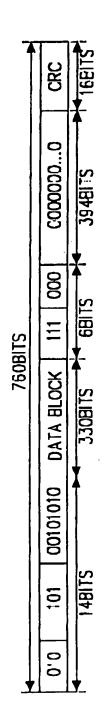
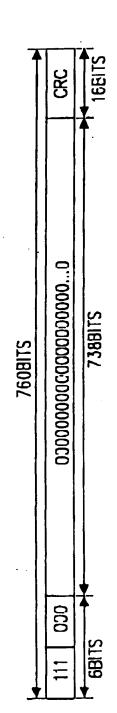


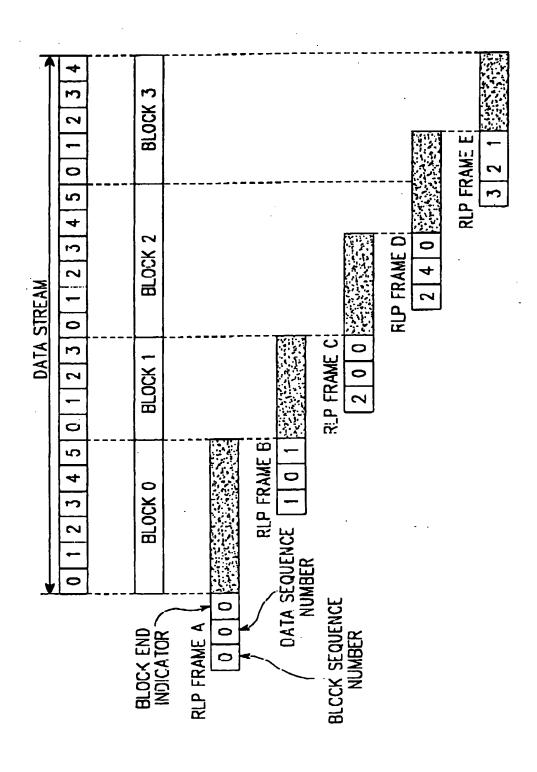
FIG. 6C

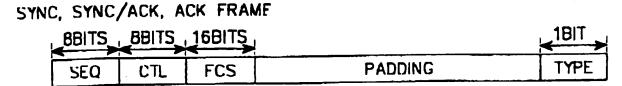


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FIG. 7





CTL='00011001' FOR SYNC,'00011010' FOR SYNC/ACK, '00011011' FOR ACK, TYPF='1'

FIG. 8A

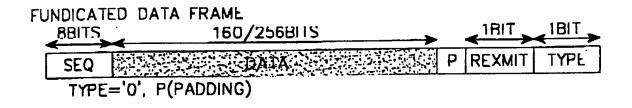


FIG. 8B

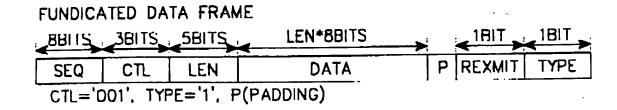


FIG. 8C

FUNDICATED DATA FRAME

| BBITS | 2BITS | 5BITS | ₩ 1BIT | 152/248BITS | | ≺ 1BIT | ✓ 1BIT |
|--------------------------------|-------|-------|--------|-------------|---|-------------------|---------------|
| SEQ | CTL | SEG | END | PATA | P | REXMIT | TYPE |
| CTL='D1', TYPE='1', P(PADDING) | | | | | | | |

FIG. 8D

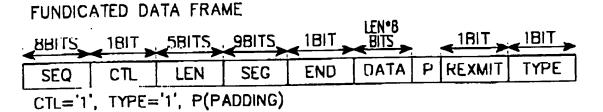


FIG. 8E

IDLE FRAME(1/8 RATE ONLY)

12BITS

SEO PADDING

FIG. 8F

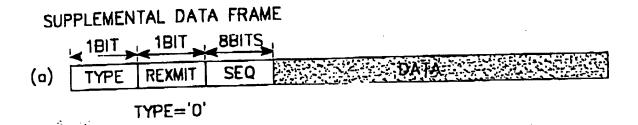


FIG. 9A

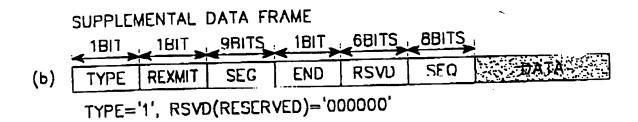
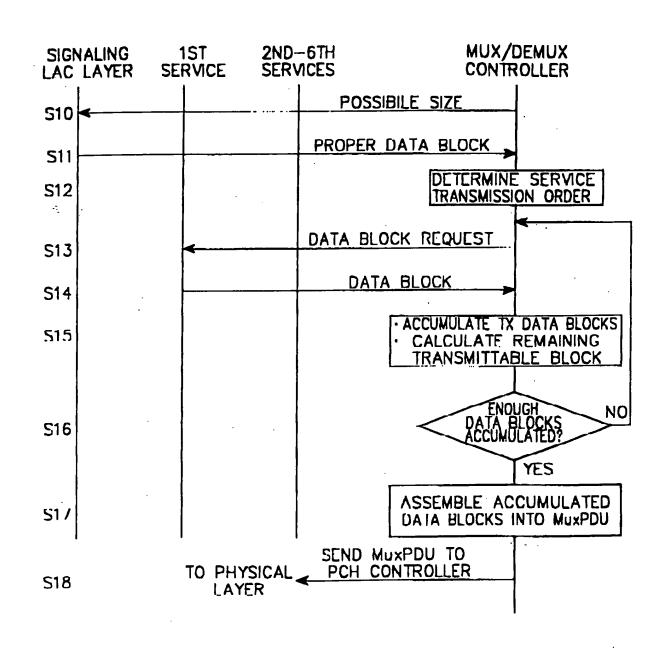


FIG. 9B



C)

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FIG. 10

FCH RECEIVING

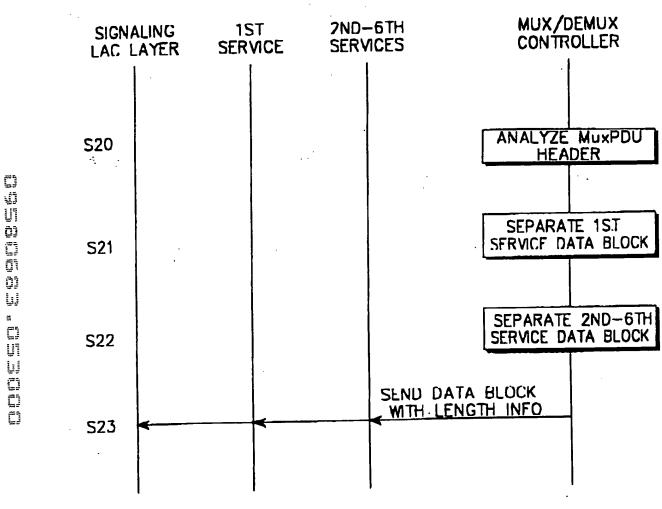


FIG. 11

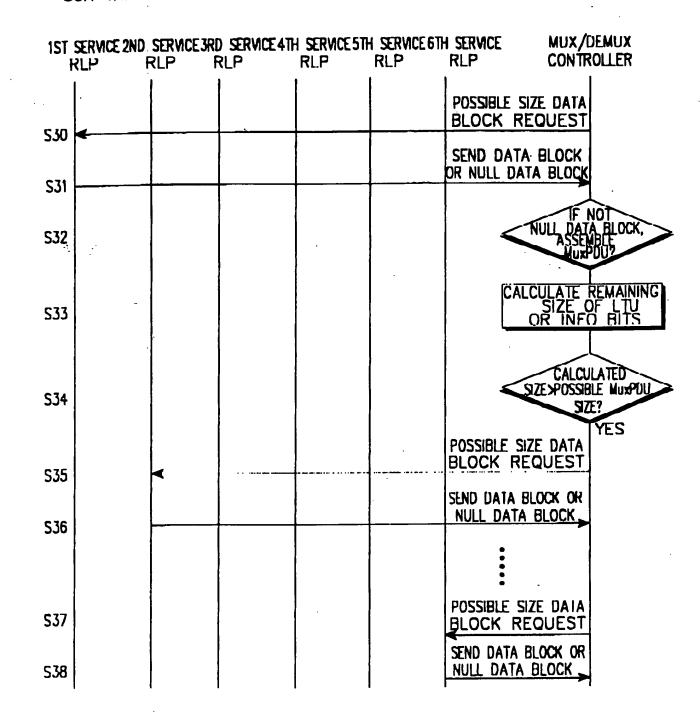


FIG. 12

SCH TRANSMISSION

6. K. F.

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C. F. S. C. C. C. C.

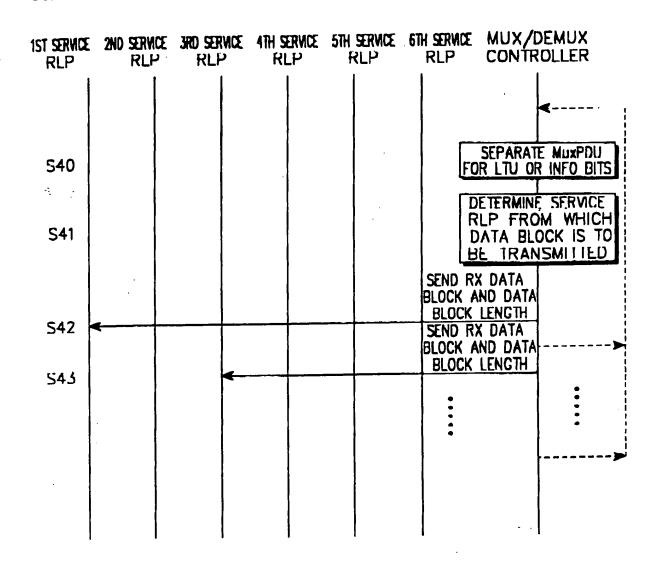


FIG. 13